

INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Docket Number (Optional) Y0R920020105US1		Application Number 10/538,935
Applicant(s) Praveen CHAUDHARI		
Filing Date June 13, 2005	Group Art Unit	2892

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

U.S. PATENT APPLICATION PUBLICATIONS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Termination	
						YES	NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

/LM/		European Search Report dated November 19, 2007
/LM/		Collier, C.P., et al., "Electronically Configurable Molecular-Based Logic Gates", Science, American Association for the Advancement of Science, US, Vol. 285, No. 5426, July 16, 1999, pp. 391-394

EXAMINER	/Lalrinlambam Malsawma/	DATE CONSIDERED	08/07/2008
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.